## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In Re Application Of   | ) Group Art Unit: 2818  | RECEIVED     |
|--|---|--------------|
| Ken Takeuchi et al.  | ) Examiner: H. Ho   | AUG 0 5 2004 |
| Serial No.: 10/073,999   | )   | ~ ~          |
| Filed: February 14, 2002   | ) Confirmation No. 9741   | OFFICIAL     |
| For: Nonvolatile Semiconductor Memory Having Plural Data Storage Portions For A Bit Line Connected To Memory Cells | ) Atty. Dkt. No. 001701.00140<br>)<br>)   |              |
| MAIL STOP AF Commissioner for Patents P.O. Box 1450  | This paper of 11 pages is being faxed to 70 on Appust 5, 2004  Christopher R. Glembocki | 3 872 9306   |

Reg. No. 38,800

Sir:

## **AMENDMENT**

In response to the final Office Action mailed May 11, 2004, please amend the instant application as follows:

Amendments to the Claims are reflected in the Listing of Claims, which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.

Alexandria, VA 22313-1450

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